

Curriculum Double Degree Master Electrical Engineering

Study plan for the start at University Siegen (winter term):

Semester	ECTS	Modul	ECTS
1 Siegen	30	Electromagnetic Field Theory (part 1)	4
		Semiconductor Electronics Design	6
		Analogue Integrated Circuits	6
		Microelectronics Sensors	6
		Advanced Analogue Circuits	6
		Advanced Semiconductors and Microelectronics	6
		Radio Frequency IC Design	6
		Microelectronics	6
		Microsystem Fabrication & Test	6
		Digital 2D/3D Image Sensors	6
		Signals and Systems I or II	6
		Photonic Devices	6
		Nanotechnology	6
		Project work or coursework (part 1)	2
2 Siegen	30	Electromagnetic Field Theory (part 2)	5
		Semiconductor Electronics	6
		Digital IC Design	6
		Optoelectronics	6
		Project work or coursework (part 2)	7
3 Vilnius	30	Microcontrollers of ARM Architecture	9
		Advanced Electronic Circuits and Systems 2 (with Course Project)	9
		Master's Research Work 3	3
		Free choice	3
		High-Frequency Circuits Design	6
		Data Mining Techniques	6
4 Vilnius	30	Evolutionary Computation and Agent Systems	6
		Master Graduation Thesis	30

compulsory

elective

Study plan for the start at VILNIUS TECH (winter term):

Semester	ECTS	Subject	ECTS
1 Vilnius	30	Fundamentals of Research and Innovations	6
		Master's Research Work 1	3
		Intelligent Systems	6
		Mathematical Modelling Technologies	6
		Signals and Signal Processing	9
2 Vilnius	30	New Micro and Nanotechnologies	6
		Advanced Electronic Circuits and Systems 1	6
		Application Specific Integrated Circuits Design (with Course Project)	9
		Master's Research Work 2	3
		Algorithms and Data Structures	6
		Systemoteknique and Sensors	6
3 Siegen	30	Modern Electric Drives	6
		Lightwave Technology	6
		Embedded Systems	6
		Microelectronics Sensors	6
		Advanced Analogue Circuits	6
		Advanced Semiconductors and Microelectronics	6
		Radio Frequency IC Design	6
		Microelectronics	6
		Microsystem Fabrication & Test	6
		Digital 2D/3D Image Sensors	6
		Signals and Systems I or II	6
		Photonic Devices	6
4 Siegen	30	Nanotechnology	6
		Master Graduation Thesis	30

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